

4 SYSTEM INTERCONNECTS

Figure 4-1. Rear Connector, J3271

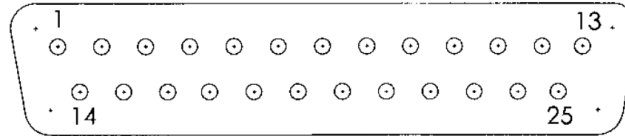


Table 4-1. P3271 Pin Assignments

Pin	Pin Name	I/O
1	AVIONICS MASTER ON	In
2	RS-232 IN 2	In
3	ALTITUDE A1	In
4	ALTITUDE C2	In
5	ALTITUDE A2	In
6	ALTITUDE A4	In
7	ALTITUDE C4	In
8	EXTERNAL IDENT INPUT*	In
9	ALTITUDE B1	In
10	ALTITUDE C1	In
11	ALTITUDE B2	In
12	ALTITUDE B4	In
13	POWER GROUND	--
14	SWITCHED POWER OUTPUT	Out
15	POWER INPUT (+11 TO +33 VDC)	In
16	EXTERNAL STANDBY*	In
17	EXTERNAL SUPPRESSION I/O	I/O
18	ALTITUDE D4	In
19	RS-232 IN 1	In
20	RS-232 OUT 1	Out
21	(RESERVED FOR SOFTWARE UPLOAD)	--
22	AIRBORNE SENSE (SQUAT SWITCH)	In
23	28 VDC PANEL LIGHTING INPUT	In
24	14 VDC/5 VDC PANEL LIGHTING INPUT	Out
25	POWER GROUND	--

* Denotes Active Low (Ground to activate).